	Application No.	Applicant(s)
Notice of Allowability		
	10/756,974 Examiner	SONG ET AL.
	CAGIIIIIei	Art Sint
	Kim T. Huynh	2112
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>4/25/06</u> .		
2. The allowed claim(s) is/are <u>1-11</u> .		
3.		
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Da 8), 7. ☐ Examiner's Amenda	te

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DETAILED ACTION

Allowable Subject Matter

1. The following is an examiner's statement of reasons for allowance:

Applicant's claimed invention is deemed allowable over the prior art of record as the prior art fails to teach or suggest a power control unit for generating signals determining whether to supply clocks and power to the respective blocks of the media access controller, in response to the control signal of the bus arbiter, the register values inputted via the local bus, and a power-save mode exiting signal provided from other blocks of the media access controller in combination with other limitations as recited in independent claim and further in view of the specification. As per Amos' reference (US Patent 6,34,870) discloses the MAC is able to utilize a bus clock to process data on the bus card. It provides a control signal to the selection low or high frequency clocks or switch to sleep mode until either the timer expires or additional bus activity is required(col.2, line 61-col.3, line 26). Where as Yik et al. (Pub No. US2003/0226050)'s reference discloses the MAC controller switch from the power-savings mode to the run mode in response to event signal (paragraph 15). Neither alone or combination of the prior arts teach a power control unit for generating signals determining whether to supply clocks and power to the respective blocks of the media access controller in response to the control signal of the bus arbiter and the register values inputted via the local bus in combination with other limitations as recited in independent claim.

2. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should 3. be directed to Kim Huynh whose telephone number is (571)272-3635 or via e-mail addressed to [kim.huynh3@uspto.gov]. The examiner can normally be reached on M-F 9.00AM- 6:00PM. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached at (571)272-3676 or via e-mail addressed to [rehana.perveen@uspto.gov].

The fax phone numbers for the organization where this application or proceeding is assigned are (571)273-8300 for regular communications and After Final communications. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (571)272-2100. SUPERVISORY PATENT EXAMINER

Kim Huynh

July 18, 2006